IN THE SPECIFICATION:

Please replace the current title with: "METHOD AND APPARATUS FOR INTERFACING A PROCESSOR TO A COPROCESSOR IN WHICH THE PROCESSOR IS CAPABLE OF SELECTIVELY BROADCASTING TO OR SELECTIVELY ALTERING AN EXECUTION MODE OF THE COPROCESSOR"

At page 3, line 9, please insert the following paragraphs:

FIG. 10 is a flow diagram that illustrates a method in accordance with one embodiment of the present invention.

FIG. 11 is a block diagram that illustrates an alternate embodiment of a portion of broadcast region control unit of FIG. 2.

At page 22, line 1, please insert the following paragraph:

FIG. 10 illustrates a flow 200 in accordance with an embodiment of the present invention. Flow 200 begins with block 202 in which a first processor receives via a coprocessor communication bus, a region indicator from a second processor, where the region indicator indicates a current execution region of the second processor. Flow proceeds to block 204 in which the execution mode of the first processor is selectively altered in response to the region indicator, where altering the execution mode of the first processor may include altering a functionality of the first processor.

At page 21, line 8, please insert the following paragraph:

FIG. 11 illustrates an alternate embodiment of a portion of broadcast region control unit 120 including region 2 base location 206, region 1 mask 208, region Y base location 210, and region Y mask location 212. Therefore, as discussed above, a base address and a mask may be used to define each broadcast region rather than a lower and upper bound.